

DETAILED ACTION

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Jaison John on 06/11/10.

2. The application has amended as follows:

IN THE ABSTRACT:

The Abstract filed on 01/05/1999 has been deleted, and the abstract has been replaced by --- The present invention provides a method and apparatus for detecting and decoding data. The method comprises: receiving a set of data signals from an external data source; detecting a size of said received set of data signals; decoding said received set of data signals; extracting a destination address from said set of data signals; comparing said destination address extracted from said data signals to a known data value; determining whether said received data signals should be received by a host circuitry based upon said comparison of said destination address extracted from said data signals to a known data value; generating at least one status signal alerting said host circuitry of said determination that said received data signals should be received by said host circuitry; and waking up said host circuitry upon a determination that said received set of data is addressed to said host circuitry. The apparatus

Art Unit: 2472

comprises: a data formatter; a clock divider; a counter; a host circuitry interface capable of transmitting and receiving data from a host circuitry; a memory circuitry; a plurality of comparators; a mask circuitry; a digital logic circuitry; a plurality of status registers; and a plurality of clocked registers. ---.

Allowable Subject Matter

3. The following is an examiner's statement of reasons for allowance:

Regarding claims 1 and 23, the prior art fails to teach a combination of the steps of:

detecting a size of said received set of data signals to use as a factor for decoding said data, in the specific combination as recited in the claims.

Regarding claim 10, the prior art fails to teach a combination of the steps of:

means for detecting a size of said received data signal;

a host circuitry interface capable of transmitting and receiving data from a host circuitry, said host circuitry enter a wake up state from a sleep mode based upon decoded address data received by said host circuitry, said decoded address data being based upon a content of said data signal and said size of said received data signals, in the specific combination as recited in the claim.

Regarding claims 32 and 34, the prior art fails to teach a combination of the steps of:

detecting a size of said received data signal to use as a factor for extracting a destination address, in the specific combination as recited in the claims.

Art Unit: 2472

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to TOAN D. NGUYEN whose telephone number is (571)272-3153. The examiner can normally be reached on M-F (7:00AM-4:30PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, William Trost can be reached on 571-272-7872. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/T. D. N./
Examiner, Art Unit 2472

/William Trost/
Supervisory Patent Examiner, Art Unit 2472